



FOR IMMEDIATE RELEASE

CALYPTECH'S INNOVATIVE MULTI-RATE PERFORMANCE MONITOR IP CORE SUPPORTS MULTIPLE TELECOM PROTOCOLS.

MELBOURNE, Victoria, April 8, 2004 - Calyptech, a leading Australian provider of innovative technological solutions, announce the release of their Multi-Rate Performance Monitor Core. This Intellectual Property (IP) core represents Calyptech's latest offering in a suite of cores designed to more fully integrate SONET/SDH, Gigabit Ethernet and Fiber Channel solutions into Field Programmable Gate Array (FPGA) technology.

Calyptech's Performance Monitor core is a leading-edge innovation. Before this solution was developed multiple devices were required to support the different telecommunications protocols. Now, Calyptech are enabling customers to reduce their costs, effort, design times, time-to-market and PCB real estate by implementing a single, flexible design that can be integrated into an FPGA to process multiple data rates while supporting SONET/SDH, Fiber Channel and Gigabit Ethernet protocols.

"This core supports data rates of up to 2.488 Gb/s and is ideally suited for the manufacture of Protocol Test Equipment, Optical Cross-connects and SONET/SDH Add-Drop Multiplexers" stated John Maher, Calyptech's CEO. "Already adopted by a leading Canadian Networking Equipment Manufacturer, our Performance Monitor core is actively reducing our customer's total expenditure and time-to-market compared to the previous solution requiring several separate devices to support each individual protocol."

About Calyptech

Calyptech (www.calyptech.com) is a leading provider of innovative technological solutions. Offering both customised products and design services, Calyptech have a proven track record in addressing difficult technical challenges in a range of telecommunications industry segments globally, including semiconductor, networking and data security.

Focused on customer success, Calyptech work in close partnership with their diverse client base, applying world class expertise in Radio Frequency, Analog, Digital Signal Processing, and high speed digital systems design, in order to deliver the most optimal solutions, ensuring reduced risk and faster time-to-market for the client.

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