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MEDIA RELEASE

**CALYPTECH ANNOUNCES 40GB/S SONET/SDH IP CORES**

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Leading Design Solutions Company Calyptech of Melbourne Australia announce the introduction of their new line of SONET/SDH cores targeted at 40Gb/s rates.

The first two cores introduced into the portfolio are an STS-768/STM-256 Framer and Data Aligner, and a parallel 256-bit Scrambler/Descrambler. The Framer and Data Aligner core accepts a 256-bit input from an SFI-5 deserializer, performs frame detection using a single-cycle search algorithm, and aligns the data to the correct byte and word boundaries. The aligned data is output from the core together with row and column counters and status signals. The Scrambler/Descrambler core contains an extremely efficient parallel implementation of the required SONET/SDH scrambler, resulting in very-low gate count and fast route speeds. Datasheets for both of these cores can be found on the Calyptech website.

Other cores currently under development for the product line include complete section and line overhead transmitters and receivers as well as B1/B2 byte generators and checkers. Future developments will include a pointer generator and processor along with complete path overhead processing cores.

**About Calyptech**

Calyptech ([www.calyptech.com](http://www.calyptech.com)) is a leading provider of innovative technological solutions. Offering both customised products and design services, Calyptech have a proven track record in addressing difficult technical challenges in a range of telecommunications industry segments globally, including semiconductor, networking and data security. Focused on customer success, Calyptech work in close partnership with their diverse client base, applying world class expertise in Radio Frequency, Analog, Digital Signal Processing, and high speed digital systems design, in order to deliver the most optimal solutions, ensuring reduced risk and faster time-to-market for the client.

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