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MEDIA RELEASE

CALYPTECH ANNOUNCES 40GB/S CRC-32 IP CORE

MELBOURNE, Victoria. World leading semiconductor and telecommunications equipment design services company, Calyptech, announce the introduction of their new CRC-32 IP Core targeted at 40Gb/s rates.

The Core uses the CRC-32 polynomial suitable for applications such as AAL5 and POS Packet error detection, processing data on a 256-bit wide bus. The Core accepts data packets with Packet Start and Packet Stop aligned to any octet boundary for maximum flexibility. The Core contains an extremely efficient parallel implementation of the required CRC-32 functionality, resulting in clock speeds in excess of 180MHz. The Datasheet for this core can be found on the Calyptech website.

Other cores currently under development targeting the high speed telecommunications product line include complete section and line overhead transmitters and receivers as well as B1/B2 byte generators and checkers. Future developments will include an SPI-4.2 to SPI-5 Bridge Core, a pointer generator and processor along with complete path overhead processing cores.

About Calyptech

Calyptech (www.calyptech.com) is a leading provider of innovative technological solutions. Offering both customised products and design services, Calyptech have a proven track record in addressing difficult technical challenges in a range of telecommunications industry segments globally, including semiconductor, networking and data security. Focused on customer success, Calyptech work in close partnership with their diverse client base, applying world class expertise in Radio Frequency, Analog, Digital Signal Processing, and high speed digital systems design, in order to deliver the most optimal solutions, ensuring reduced risk and faster time-to-market for the client.

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Contact:

CALYPTECH

John Maher

Chief Executive Officer

Ph. +61 3 9455 1290

Fax. +61 3 9459 9966

Email: john.maher@calyptech.com